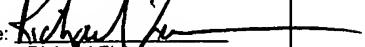


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Dated: July 9, 2003

Signature: 

Docket No.: 29926/39502
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Won-Ho Lee

Application No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filed: July 9, 2003 (concurrently herewith)

Examiner: Not Yet Assigned

For: **METHOD FOR ISOLATING HYBRID
DEVICE IN IMAGE SENSOR**

INFORMATION DISCLOSURE STATEMENT (IDS)

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Dated: July 9, 2003

Respectfully submitted,

By Marla L. Hudson
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				Application Number	Not Yet Assigned
				Filing Date	July 9, 2003
				First Named Inventor	Won-Ho Lee
				Art Unit	Not Yet Assigned
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	29926/39502

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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		Fujieda et al., "Dependence of Si PN Junction Perimeter Leakage on the Channel-Stop Boron Dose and Interlayer Material" (IEEE ELECTRON DEVICE LETTERS, VOL. 20, NO. 8, AUGUST 1999, Pages 418-420)			
		Morris et al., "An Accurate and Efficient Model for Boron Implants Through Thin Oxide Layers into Single-Crystal Silicon" (IEEE TRANSACTIONS ON SEMICONDUCTOR MANUFACTURING, VOL. 8, NO. 4, NOVEMBER 1995, Pages 408-413)			
		Walter et al., "Dopant Channeling as a Function of Implant Angle for Low Energy Applications" (IEEE, 1999, Pages 126-129)			
		Son et al., "Blanket Tilt Implanted Shallow Trench Isolation (BTI-STI) Process for Enhanced DRAM Retention Time Characteristics" (IEEE, 1999, Pages 122-124)			

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